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23494 7590 2/17/2005

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Sharlet Hurst (Depositor's name)
Sharlet Hurst (Signature)
5-9-05 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/727,268	12/2/2003	Indumini Ranmuthu	T1-36028	4910

TITLE OF INVENTION: **DAMPING RESISTOR BOOST WRITER ARCHITECTURE**

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	5/17/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
Faber, Alan	2651	360-046000

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5-9-2005

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PTO-85 (REV. 04-02) Approved for use through 01/31/2004. OMB 0651-0033

05/10/2005 AWDNDAF2 00000093 200668 10727268

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA



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From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
First Inventor: Ranmuthu

Docket Number: TI-36028

Serial No.: 10/727,268

Art Unit: 2651

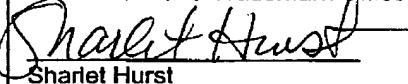
Filed: 12/02/03

Examiner: Faber, Alan

For: DAMPING RESISTOR BOOST WRITER ARCHITECTURE

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NAME OF INVENTOR(S): Ranmuthu		
TITLE OF INVENTION: DAMPING RESISTOR BOOST WRITER ARCHITECTURE		
TI FILE NO.: TI-36028	DEPOSIT ACCT. NO.: 20-0668	RECEIPT DATE & SERIAL NO.: Serial No.: 10/727,268 Filing Date: 12/02/2003
FAXED: 05/09/05 DUE: 05/17/05 ATTY/SECY:WDS/slh		

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